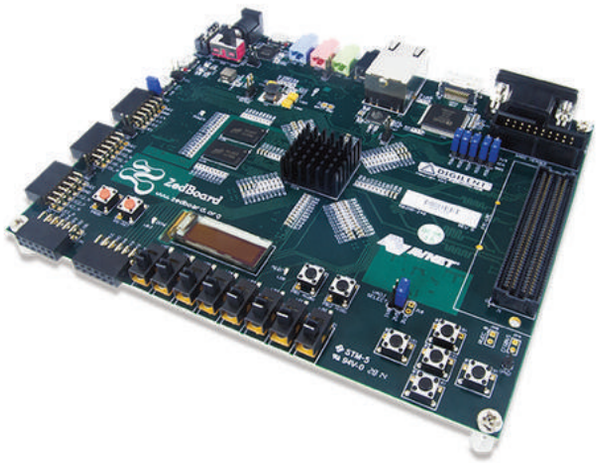
Zynq SoC ARM / FPGA Board

ZedBoard

Pmod Peripheral Modules
Compatible Pmod

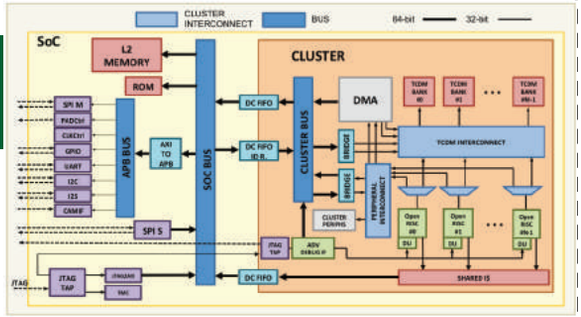


- Zynq-7000 AP SoC XC7Z020-CLG484
- Memory: 512 MB DDR3, 256 Mb Quad-SPI Flash, 4 GB SD card
- Video I/O: 1080p HDMI, 8-bit VGA
- I2S Audio CODEC
- 10/100/1000 Ethernet PHY
- USB OTG 2.0 and USB-UART
- 128 x 32 OLED Display
- Low-pin count (LPC) FMC connector
- Six Pmod™ ports including one for analog-to-digital converter (XADC)
- GPIO: 6 pushbuttons, 8 slide switches, 8 LEDs
- Onboard USB-JTAG Programming
- Includes Micro-USB cable, 12 V AC/DC power supply (UK plug adapter not included)

ZedBoard is built around Zynq-7000 AP SoC XC7Z020-CLG484. This board contains everything necessary to create a Linux, Android, Windows® or other OS/RTOS based design. Additionally, several expansion connectors expose the processing system and programmable logic I/Os for easy user access. User can start first hardware design on Zynq through getting started guide

Free 32-bit processor (PULP) core based on RISC-V plus peripherals with ZedBoard

PULP (Parallel Ultra-Low-Power Processing-Platform) is a joint project between the Integrated Systems laboratory (IIS) of ETH Zurich (IIS) and the Energy- efficient Embedded Systems (EEES) group of UNIBO to develop an open, scalable hardware and software research platform.
Available at http://www.pulp-platform.org/



Related Items

Zynq SDSoC voucher

- This voucher is only for use with the ZedBoard or Zybo. To download SDSoC development too, please visit http://www.xilinx.com/products/design-tools/software-zone/sdsoc.html